

Thu, 23 Feb 2006, 5:10:33 PM EST

Edit an existing query or
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Search Query Display.

Search Query Display

Select a search number (#)
to:

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Recent Search Queries

- [#1](#) ((lock-free multiprocessor)<in>metadata)
- [#2](#) ((lock-free multi-processor)<in>metadata)
- [#3](#) ((lock free multi-processor)<in>metadata)
- [#4](#) ((lock free multi processor)<in>metadata)
- [#5](#) (shared memory<IN>metadata)
- [#6](#) (((shared memory<in>metadata)<AND>(lock<in>metadata))
- [#7](#) (((shared memory<in>metadata)<AND>(lock<in>metadata))
- [#8](#) ((((((shared memory<in>metadata)<and>(lock<in>metadata)))<AND>(critical<in>metadata))
- [#9](#) ((((((shared memory<in>metadata)<and>(lock<in>metadata)))<AND>(critical<in>metadata))
- [#10](#) (concurrency control<in>de)
- [#11](#) (concurrency control<in>de)
- [#12](#) (shared-memory multiprocessor machines<in>de)
- [#13](#) (non block multiprocessor<in>metadata)
- [#14](#) (non-block<IN>metadata)
- [#15](#) (non-block<IN>metadata)
- [#16](#) (((non-block<in>metadata)<AND>(shared memory<in>metadata))
- [#17](#) (((non-block<in>metadata)<AND>(shared memory<in>metadata))
- [#18](#) (((non-block<in>metadata)<AND>(shared memory<in>metadata))
- [#19](#) (((non-block<in>metadata)<AND>(shared memory<in>metadata))
- [#20](#) (lock-free algorithm<in>de)

PLUS Search Results for S/N 10775448, Searched February 23, 2006

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

6684398
6460124
5321825
5742785
5623671
5274823
6094663
5797026
5606703
5404482
5237694
5613153
5289588
5933856
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6473849
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5446862
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5745758
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5867712
5983004
6249526
5530884
5404386
5848288
6820263
5694553
6941449
6003061
6112272
5668975
5828861

UW-Madison Computer Architecture Group

Department of Computer Sciences and Department of
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Faculty &
Projects

Highlights

Students

Weekly
Seminar

Courses

Qualifying
Exam

Industrial
Affiliates
Program

Alumni

Architecture Ph.D. Alumni

- Ravi Rajwar - September 2002
Advisor: Jim Goodman
Thesis title: *Speculation-Based Techniques for Transactional Lock-Free Execution of Lock-Based Programs*
- Craig Zilles - August 2002
Advisor: Guri Sohi
Thesis title: *Master/Slave Speculative Parallelization and Approximate Code*
- Daniel Sorin - ECE - August 2002
Advisor: David Wood
Thesis title: *Using Lightweight Checkpoint/Recovery to Improve the Availability and Designability of Shared Memory Multiprocessors*
- Timothy Heil - ECE - August 2002
Advisor: Jim Smith
Thesis title: *Relational Profiling in Multithreaded Virtual Machines*
- Amir Roth - August 2001
Advisor: Guri Sohi
Thesis title: *Pre-Execution via Speculative Data-Driven Multithreading*
- Avinash Sodani - March 2000
Advisor: Guri Sohi
Thesis title: *Dynamic Instruction Reuse*
- Eric Rotenberg - September 1999
Advisor: Jim Smith
Thesis title: *Trace Processors: Exploiting Hierarchy and Speculation*
- Quinn Jacobson - ECE - August 1999
Advisor: Jim Smith
Thesis title: *High-Performance Front-ends for Trace Processors*
- Alain Kägi - January 1999
Advisor: Jim Goodman
Thesis title: *Mechanisms for Efficient Shared-memory, Lock-based Synchronization*
- Andreas Moshovos - December 1998
Advisor: Guri Sohi
Thesis title: *Memory Dependence Prediction*
- Doug Burger - December 1998

Last modified: Tuesday, 03-Dec-2002 20:40:01 CST
Maintained by Milo Martin
